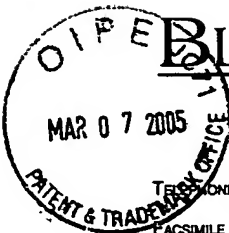


101052,853.

copy



BLAKELY SOKOLOFF TAYLOR & ZAFMAN

A LIMITED LIABILITY PARTNERSHIP INCLUDING LAW CORPORATIONS

TELEPHONE (408) 720-8300

FACSIMILE (408) 720-8383

BSTZ_MAIL@BSTZ.COM

WWW.BSTZ.COM

INTELLECTUAL PROPERTY LAW

SILICON VALLEY

1279 OAKMEAD PARKWAY
SUNNYVALE, CALIFORNIA 94085-4040

OTHER OFFICES

LOS ANGELES, CA
ORANGE COUNTY/COSTA MESA, CA
SAN JOSE, CA
DENVER, CO
PORTLAND/BEAVERTON, OR
SEATTLE, WA

February 24, 2005

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Certificate
MAR 15 2005
of Correction

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 6,849,896
Issued: February 1, 2005
For: **FLASH MEMORY WITH UV OPAQUE**
PASSIVATION LAYER
Inventor: Wada et al.
Our File No. 42390.P7196d

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Reg. No. 25,129

EHT/jsq
Enclosures

MAR 15 2005

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,849,896
DATED : February 1, 2005
INVENTOR(S) : Wada et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In (57), line 3 of the abstract after "semiconductor", insert --substrate--.
In (57), line 4 of the abstract after "a", insert --conductive layer on--.
In (57), line 7 of the abstract delete "no" and insert --not--.

MAILING ADDRESS OF SENDER
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN
12400 Wilshire Blvd. 7th floor
Los Angeles, CA 90025-1026

PATENT NO. 6,849,896

Certificate of Correction (PTO Form 1050)-Amended

MAR 15 2005

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,849,896
DATED : February 1, 2005
INVENTOR(S) : Wada et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In (57), line 3 of the abstract after "semiconductor", insert --substrate--.
In (57), line 4 of the abstract after "a", insert --conductive layer on--.
In (57), line 7 of the abstract delete "no" and insert --not--.

MAILING ADDRESS OF SENDER
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN
12400 Wilshire Blvd. 7th floor
Los Angeles, CA 90025-1026

PATENT NO. 6,849,896

Certificate of Correction (PTO Form 1050)-Amended

MAR 15 2005